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21 [CCured: type-safe retrofitting of legacy software](#)

George C. Necula, Jeremy Condit, Matthew Harren, Scott McPeak, Westley Weimer
 May 2005 **ACM Transactions on Programming Languages and Systems (TOPLAS)**,
 Volume 27 Issue 3

Full text available:  [pdf\(613.66 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This article describes CCured, a program transformation system that adds type safety guarantees to existing C programs. CCured attempts to verify statically that memory errors cannot occur, and it inserts run-time checks where static verification is insufficient. CCured extends C's type system by separating pointer types according to their usage, and it uses a surprisingly simple type inference algorithm that is able to infer the appropriate pointer kinds for existing C programs. CCured uses phys ...

Keywords: Memory safety, libraries, pointer qualifier, subtyping

22 [Information systems outsourcing: a survey and analysis of the literature](#)

Jens Dibbern, Tim Goles, Rudy Hirschheim, Bandula Jayatilaka
 November 2004 **ACM SIGMIS Database**, Volume 35 Issue 4

Full text available:  [pdf\(1.51 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)

In the last fifteen years, academic research on information systems (IS) outsourcing has evolved rapidly. Indeed the field of outsourcing research has grown so fast that there has been scant opportunity for the research community to take a collective breath, and complete a global assessment of research activities to date. This paper seeks to address this need by exploring and synthesizing the academic literature on IS outsourcing. It offers a roadmap of the IS outsourcing literature, highlight ...

Keywords: determinants, literature review, outcomes, outsourcing, relationships, research approaches, theoretical foundations

23 [Fast detection of communication patterns in distributed executions](#)

Thomas Kunz, Michiel F. H. Seuren
 November 1997 **Proceedings of the 1997 conference of the Centre for Advanced Studies on Collaborative research**

Full text available:  [pdf\(4.21 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Understanding distributed applications is a tedious and difficult task. Visualizations based on

process-time diagrams are often used to obtain a better understanding of the execution of the application. The visualization tool we use is Poet, an event tracer developed at the University of Waterloo. However, these diagrams are often very complex and do not provide the user with the desired overview of the application. In our experience, such tools display repeated occurrences of non-trivial commun ...

24 Practical extraction techniques for Java

Frank Tip, Peter F. Sweeney, Chris Laffra, Aldo Eisma, David Streeter

November 2002 **ACM Transactions on Programming Languages and Systems (TOPLAS)**,

Volume 24 Issue 6

Full text available:  [pdf\(1.01 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Reducing application size is important for software that is distributed via the internet, in order to keep download times manageable, and in the domain of embedded systems, where applications are often stored in (Read-Only or Flash) memory. This paper explores extraction techniques such as the removal of unreachable methods and redundant fields, inlining of method calls, and transformation of the class hierarchy for reducing application size. We implemented a number of extraction techniques in < ...

Keywords: Application extraction, call graph construction, class hierarchy transformation, packaging, whole-program analysis

25 Equivalence verification: Automated equivalence checking of switch level circuits

Simon Jolly, Atanas Parashkevov, Tim McDougall

June 2002 **Proceedings of the 39th conference on Design automation**

Full text available:  [pdf\(220.14 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

A chip that is required to meet strict operating criteria in terms of speed, power, or area is commonly custom designed at the switch level. Traditional techniques for verifying these designs, based on simulation, are expensive in terms of resources and cannot completely guarantee correct operation. Formal verification methods, on the other hand, provide for a complete proof of correctness, and require less effort to setup. This paper presents Motorola's Switch Level Verification (SLV) tool, whi ...

Keywords: MOS circuits, VLSI design, custom design, equivalence checking, formal verification, switch level analysis

26 IS '97: model curriculum and guidelines for undergraduate degree programs in information systems

Gordon B. Davis, John T. Gorgone, J. Daniel Couger, David L. Feinstein, Herbert E. Longenecker

December 1996 **ACM SIGMIS Database , Guidelines for undergraduate degree programs on Model curriculum and guidelines for undergraduate degree programs in information systems**, Volume 28 Issue 1

Full text available:  [pdf\(7.24 MB\)](#) Additional Information: [full citation](#), [citations](#)

27 Special issue: AI in engineering

D. Sriram, R. Joobani

January 1985 **ACM SIGART Bulletin**, Issue 91

Full text available:  [pdf\(8.79 MB\)](#) Additional Information: [full citation](#), [abstract](#)

The papers in this special issue were compiled from responses to the announcement in the

July 1984 issue of the SIGART newsletter and notices posted over the ARPAnet. The interest being shown in this area is reflected in the sixty papers received from over six countries. About half the papers were received over the computer network.

28 Ethical accountability in the cyberspace

K. L. Nance, M. Strohmaier

November 1994 **Proceedings of the conference on Ethics in the computer age**

Full text available: [!\[\]\(cbe2492b119e39e02a1dab2af4a4b296_img.jpg\) pdf\(636.88 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Conformance with ethical behavior consists of adherence to the standards of conduct for any given group. When standards are not formalized, there can exist ethical disparity from which many diverse problems can result. These problems are especially evident in the cyberspace. Within the cyberspace, the "given group" is culturally and ethnically diverse. As such, it is difficult to hold the individuals to a nonformalized set of standards. Several important issues need to be addres ...

29 System-level power optimization: techniques and tools

Luca Benini, Giovanni de Micheli

April 2000 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**,

Volume 5 Issue 2

Full text available: [!\[\]\(0d5ec72f61334709c3fc9450209b754f_img.jpg\) pdf\(385.22 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This tutorial surveys design methods for energy-efficient system-level design. We consider electronic systems consisting of a hardware platform and software layers. We consider the three major constituents of hardware that consume energy, namely computation, communication, and storage units, and we review methods of reducing their energy consumption. We also study models for analyzing the energy cost of software, and methods for energy-efficient software design and compilation. This survey ...

30 A structural view of the Cedar programming environment

Daniel C. Swinehart, Polle T. Zellweger, Richard J. Beach, Robert B. Hagmann

August 1986 **ACM Transactions on Programming Languages and Systems (TOPLAS)**,

Volume 8 Issue 4

Full text available: [!\[\]\(28f72b996fc97883dfd9d4e8b1b16b4e_img.jpg\) pdf\(6.32 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper presents an overview of the Cedar programming environment, focusing on its overall structure—that is, the major components of Cedar and the way they are organized. Cedar supports the development of programs written in a single programming language, also called Cedar. Its primary purpose is to increase the productivity of programmers whose activities include experimental programming and the development of prototype software systems for a high-performance personal computer. T ...

31 Human-computer interface development: concepts and systems for its management

H. Rex Hartson, Deborah Hix

March 1989 **ACM Computing Surveys (CSUR)**, Volume 21 Issue 1

Full text available: [!\[\]\(4c9516d2c24d0d513bc9f84c2e013d65_img.jpg\) pdf\(7.97 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Human-computer interface management, from a computer science viewpoint, focuses on the process of developing quality human-computer interfaces, including their representation, design, implementation, execution, evaluation, and maintenance. This survey presents important concepts of interface management: dialogue independence, structural modeling, representation, interactive tools, rapid prototyping, development methodologies, and control structures. *Dialogue independence* is th ...

32 Automated design and analysis system for design of custom orthopedic implants

R. L. Dooley, G. Heimke, Ajit Dingankar, E. Berg, E. Kimbrough

June 1988 **Proceedings of the first international conference on Industrial and engineering applications of artificial intelligence and expert systems - Volume 1**

Full text available: [pdf\(738.77 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

A parametric model for implant design has been developed and incorporated into an expert system for prosthesis design and manufacturing. The expert system acquires data concerning the physical characteristics of a patient. The system conceptualizes a space-filling custom design based upon the available data and calculations. The system considers existing off-the-shelf designs and the custom design as parallel alternatives and applies geometric criteria to eliminate designs that are incompat ...

33 Spatial computation

Mihai Budiu, Girish Venkataramani, Tiberiu Chelcea, Seth Copen Goldstein

October 2004 **Proceedings of the 11th international conference on Architectural support for programming languages and operating systems**, Volume 32 , 39 , 38 Issue 5 , 11 , 5

Full text available: [pdf\(573.00 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This paper describes a computer architecture, *Spatial Computation* (SC), which is based on the translation of high-level language programs directly into hardware structures. SC program implementations are completely distributed, with no centralized control. SC circuits are optimized for wires at the expense of computation units. In this paper we investigate a particular implementation of SC: ASH (Application-Specific Hardware). Under the assumption that computation is cheaper than co ...

Keywords: application-specific hardware, dataflow machine, low-power, spatial computation

34 Synthesis and timing analysis for FPGAs: Evaluating heuristics in automatically mapping multi-loop applications to FPGAs

Heidi Ziegler, Mary Hall

February 2005 **Proceedings of the 2005 ACM/SIGDA 13th international symposium on Field-programmable gate arrays**

Full text available: [pdf\(560.54 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This paper presents a set of measurements which characterize the design space for automatically mapping high-level algorithms consisting of multiple loop nests, expressed in C, onto an FPGA. We extend a prior compiler algorithm that derived optimized FPGA implementations for individual loop nests. We focus on the space-time tradeoffs associated with sharing constrained chip area among multiple computations represented by an asynchronous pipeline. Intermediate results are communicated on chip; co ...

Keywords: FPGAs, hardware design, high-level and architectural synthesis, parallelizing compiler analysis techniques, pipelining, rapid prototyping, synthesis techniques for configurable computing

35 Testing and Debugging Custom Integrated Circuits

Edward H. Frank, Robert F. Sproull

December 1981 **ACM Computing Surveys (CSUR)**, Volume 13 Issue 4

Full text available: [pdf\(2.25 MB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

36 Relaxed MultiJava: balancing extensibility and modular typechecking

Todd Millstein, Mark Reay, Craig Chambers

October 2003 **ACM SIGPLAN Notices , Proceedings of the 18th annual ACM SIGPLAN conference on Object-oriented programming, systems, languages, and applications**, Volume 38 Issue 11

Full text available:  [pdf\(162.17 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We present the rationale, design, and implementation of Relaxed MultiJava (RMJ), a backward-compatible extension of Java that allows programmers to add new methods to existing classes and to write multimethods. Previous languages supporting these forms of extensibility either restrict their usage to a limited set of programming idioms that can be modularly typechecked (and modularly compiled) or simply forego modular typechecking altogether. In contrast, RMJ supports the new language features in ...

Keywords: class loader, external methods, modular typechecking, multimethods, relaxed MultiJava

37 A design flow for partially reconfigurable hardware

Ian Robertson, James Irvine

May 2004 **ACM Transactions on Embedded Computing Systems (TECS)**, Volume 3 Issue 2

Full text available:  [pdf\(698.30 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This paper presents a top-down designer-driven design flow for creating hardware that exploits partial run-time reconfiguration. Computer-aided design (CAD) tools are presented, which complement conventional FPGA design environments to enable the specification, simulation (both functional and timing), synthesis, automatic placement and routing, partial configuration generation and control of partially reconfigurable designs. Collectively these tools constitute the dynamic circuit switching CAD f ...

Keywords: FPGA, Viterbi decoder, configuration control, dynamically reconfigurable logic (DRL), power estimation, run-time reconfiguration (RTR)

38 A field study of exploratory learning strategies

John Rieman

September 1996 **ACM Transactions on Computer-Human Interaction (TOCHI)**, Volume 3 Issue 3

Full text available:  [pdf\(259.01 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

It has suggested that interactive computer users find "exploratory learning" to be an effective and attractive strategy for learning a new system or investigating unknown features of familiar software. In exploratory learning, instead of working through precisely sequenced training materials, the user investigates a system on his or her own initiative, often in pursuit of a real or artificial task. The value of exploratory learning has been studied in controlled settings, with s ...

Keywords: diary studies, discovery learning, exploratory learning, learning in the workplace, learning on demand

39 Demonstrational and constraint-based techniques for pictorially specifying application objects and behaviors

Brad Vander Zanden, Brad A. Myers

December 1995 **ACM Transactions on Computer-Human Interaction (TOCHI)**, Volume 2
Issue 4

Full text available:  [pdf\(3.70 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

The Lapidary interface design tool is a demonstrational system that allows the graphics and run-time behaviors that go inside an application window to be specified pictorially. In particular, Lapidary allows the designer to draw example pictures of application-specific graphical objects that the end user will manipulate (such as boxes, arrows, or elements of a list), the feedback that shows which objects are selected (such as small boxes on the sides and corners of an objec ...

Keywords: direct manipulation, interaction, interaction techniques, object-oriented design, programming by example, user interface management systems

40 The management of end-user computing: status and directions



James C. Brancheau, Carol V. Brown

December 1993 **ACM Computing Surveys (CSUR)**, Volume 25 Issue 4

Full text available:  [pdf\(3.74 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The development of computing applications by the people who have direct need for them in their work has become commonplace. During the 1980s, development of applications by "end users" accelerated and became a key management and research concern. Known as "end-user computing," the phenomena and research associated with this trend cross a variety of disciplines. This article critically surveys the published literature on end-user computing (EUC) management according t ...

Keywords: desktop computing, end-user computing, information center, information technology management, personal computing

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- Engenium's Semetric query engine is an advanced conceptual search engine, documents regardless of structure, content, or length. Semetric moves search keyword queries by inferring the meaning of words based on the context in which they appear. Not only are conceptual relationships measured between words, but also the document is considered to return the most relevant matches available.
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 - Note that while all documents are presented to the Semetric engine, at present, only English and European documents are reliably indexed.

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- Queries to Semetric have neither a specific syntax nor reserved-word operators. Instead, they are one or more phrases or sentences that describe the desired concept. A simple example of an appropriate Semetric query is "semiconductor fabrication". Other examples include "wireless interoperability specification" and "A flip-chip technology, denominated as DDFC (Downset Flip-Chip) technology, is characterized by the forming of a central hole in the substrate, and by the use of an array of solder bumps over the top surface of the semiconductor chip and an array of recessed solder-bump pads of an inverted conical shape over the bottom surface of the device hole for connecting the semiconductor chip to the substrate" (from US Patent #6,507,119).
- Note that while all documents are presented to the Semetric engine, at present European documents are reliably indexed.

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Result # 1 Relevance: **Display of Nulls Fields for Input and Output**

1989-06-01 IPCOM000035128D English

Disclosed is a method for allowing the user to select a character to visually differentiate and blank values when viewing or inputting data on a screen.

Result # 2 Relevance: **Method for Copying Differently Formatted Records With a Field Processor**

1988-05-01 IPCOM000057408D English

This method accomplishes the copying of records of one format to records of another field processor and a single buffer for input and output and with padding or truncation in place. A field processor takes as input a record identifier, a list of ...

Result # 3 Relevance: **Table-Driven Audit**

1985-06-01 IPCOM000064115D English

This article describes the design of a table-driven audit for input records sorted into Data (DL/I) hierarchical order (inverted tree structure). The audit characteristics are: audit input from an external table; all input records need not have the ...

Result # 4 Relevance: **Allow User to Specify Field by Using Either Field Name or Field Number**

1982-08-01 IPCOM000051251D English

Field name correspondence problems are overcome in applying different setups to different through providing an operator with the capability of specifying field names in addition to field numbers.

Result # 5 Relevance: **Display Process for Large Output Only Fields**

1990-08-01 IPCOM000103195D English

Disclosed is a simple mechanism that will allow the user to magnify output only fields to data contained within it. Applications frequently want to display information of use to a user associated screen real estate problems do not always allow the ...

Result # 6 Relevance: **Field Outlining Audit Operation**

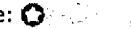
1986-06-01 IPCOM000061106D English

This article describes a field outlining audit operation for application generators. Information management system application development programs, such as IMSADF II, use an audit to validate input data, perform application logic, and process application data. ...

Result # 7 Relevance: **Priority Merger of Record Fields**

1972-09-01 IPCOM000077704D English

Two or more records can be merged to produce a single record in which nonnull fields a predetermined order. Individual fields of each record are given a priority. This priorit resolve any conflicts when data exists in more than one of the input ...

Result # 8 Relevance:  100

Symbolic Language Translation Using a Data Structure Input

1971-12-01 IPCOM000075930D English

The Figure illustrates the sequential processing of a direct implementation of the basic language process. This implementation is in the form of a one pass, branchless proced

Result # 9 Relevance:  100

Table-Driven Trace Record Formatting

1987-10-01 IPCOM000040318D English

In a VM/SP operating system, in order to do operations such as multi- tasking, and share common storage by a group of virtual machines, one needs a way to collect trace information that must be done in a way that produces current and identifiable information. Tracing ...

Result # 10 Relevance:  100

Selection of Alternative Subfields for Table-Driven Trace Record Form

1987-10-01 IPCOM000040337D English

When processing table-driven code, it is often necessary to select certain parts of the table, particularly useful with respect to a trace entry table. The control mechanism described in the following branch capabilities: 1) It allows for selection ...

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<u>L5</u> L1 and (form same (fill in or fill-in))	28	<u>L5</u>
<u>L4</u> L3 and (default same (behavior\$4 or process\$2))	4	<u>L4</u>
<u>L3</u> L2 and (form same (fill in or fill-in))	17	<u>L3</u>
<u>L2</u> L1 and (form same field\$1)	2959	<u>L2</u>
<u>L1</u> electronic and (custom same (behavior\$4 or process\$2))	12841	<u>L1</u>

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1. Document ID: US 20040078373 A1

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File: PGPB

Apr 22, 2004

PGPUB-DOCUMENT-NUMBER: 20040078373

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040078373 A1

TITLE: Workflow system and method

PUBLICATION-DATE: April 22, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Ghoneimy, Adel	Hillsborough	CA	US	
Zinatbaksh, Ali	San Jose	CA	US	
Tiwari, Sandeep	Los Gatos	CA	US	
Wein, Jerry D.	San Jose	CA	US	
Jun, Andrew	San Jose	CA	US	
Simhadri, Ruby S.	San Jose	CA	US	

US-CL-CURRENT: 707/10

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2. Document ID: US 20040077090 A1

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File: PGPB

Apr 22, 2004

PGPUB-DOCUMENT-NUMBER: 20040077090

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040077090 A1

TITLE: Whole cell engineering by mutagenizing a substantial portion of a starting genome, combining mutations, and optionally repeating

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMIC	Drawn D
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3. Document ID: US 6094684 A

L4: Entry 3 of 4

File: USPT

Jul 25, 2000

US-PAT-NO: 6094684

DOCUMENT-IDENTIFIER: US 6094684 A

TITLE: Method and apparatus for data communication

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L4: Entry 4 of 4

File: USPT

Dec 21, 1999

US-PAT-NO: 6006242

DOCUMENT-IDENTIFIER: US 6006242 A

TITLE: Apparatus and method for dynamically creating a document

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File: PGPB

Apr 21, 2005

PGPUB-DOCUMENT-NUMBER: 20050086587

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20050086587 A1

TITLE: System and method for presenting computerized interactive forms to respondents using a client-server-systems technology based on web standards

PUBLICATION-DATE: April 21, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Balz, Christopher Mark	Palo Alto	CA	US	

US-CL-CURRENT: 715/505

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Drawn D
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2. Document ID: US 20050081141 A1

L12: Entry 2 of 29

File: PGPB

Apr 14, 2005

PGPUB-DOCUMENT-NUMBER: 20050081141

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20050081141 A1

TITLE: Visual programming system and method

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Drawn D
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3. Document ID: US 20050060644 A1

L12: Entry 3 of 29

File: PGPB

Mar 17, 2005

PGPUB-DOCUMENT-NUMBER: 20050060644

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20050060644 A1

TITLE: Real time variable digital paper

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Drawn](#)

4. Document ID: US 20050055627 A1

L12: Entry 4 of 29

File: PGPB

Mar 10, 2005

PGPUB-DOCUMENT-NUMBER: 20050055627

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20050055627 A1

TITLE: System and method for personalizing electronic mail messages

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Drawn](#)

5. Document ID: US 20050041266 A1

L12: Entry 5 of 29

File: PGPB

Feb 24, 2005

PGPUB-DOCUMENT-NUMBER: 20050041266

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20050041266 A1

TITLE: Printer for composing and delivering electronic mail

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Drawn](#)

6. Document ID: US 20050028084 A1

L12: Entry 6 of 29

File: PGPB

Feb 3, 2005

PGPUB-DOCUMENT-NUMBER: 20050028084

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20050028084 A1

TITLE: System and method for a form validation engine

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Drawn](#)

7. Document ID: US 20040268229 A1

L12: Entry 7 of 29

File: PGPB

Dec 30, 2004

PGPUB-DOCUMENT-NUMBER: 20040268229

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040268229 A1

TITLE: Markup language editing with an electronic form

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KM/C	Drawn D
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8. Document ID: US 20040205525 A1

L12: Entry 8 of 29

File: PGPB

Oct 14, 2004

PGPUB-DOCUMENT-NUMBER: 20040205525

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040205525 A1

TITLE: Automatic identification of form contents

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KM/C	Drawn D
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9. Document ID: US 20030172347 A1

L12: Entry 9 of 29

File: PGPB

Sep 11, 2003

PGPUB-DOCUMENT-NUMBER: 20030172347

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030172347 A1

TITLE: File transforming output engine having a decomposer and multiple writers

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KM/C	Drawn D
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10. Document ID: US 20030078949 A1

L12: Entry 10 of 29

File: PGPB

Apr 24, 2003

PGPUB-DOCUMENT-NUMBER: 20030078949

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030078949 A1

TITLE: Automatic generation of forms with input validation

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KM/C	Drawn D
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11. Document ID: US 20030023626 A1

L12: Entry 11 of 29

File: PGPB

Jan 30, 2003

PGPUB-DOCUMENT-NUMBER: 20030023626

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030023626 A1

TITLE: Apparatus and method for creating customized business forms

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KM/C	Drawn D
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 12. Document ID: US 20030023625 A1

L12: Entry 12 of 29

File: PGPB

Jan 30, 2003

PGPUB-DOCUMENT-NUMBER: 20030023625

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030023625 A1

TITLE: System and method for completing forms

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMNC	Draw. D
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 13. Document ID: US 20020129054 A1

L12: Entry 13 of 29

File: PGPB

Sep 12, 2002

PGPUB-DOCUMENT-NUMBER: 20020129054

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020129054 A1

TITLE: Method and system for integrating network-based functionality into productivity applications employing spreadsheets

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMNC	Draw. D
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 14. Document ID: US 20020069220 A1

L12: Entry 14 of 29

File: PGPB

Jun 6, 2002

PGPUB-DOCUMENT-NUMBER: 20020069220

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020069220 A1

TITLE: REMOTE DATA ACCESS AND MANAGEMENT SYSTEM UTILIZING HANDWRITING INPUT

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMNC	Draw. D
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 15. Document ID: US 6920608 B1

L12: Entry 15 of 29

File: USPT

Jul 19, 2005

US-PAT-NO: 6920608

DOCUMENT-IDENTIFIER: US 6920608 B1

TITLE: Chart view for reusable data markup language

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMNC	Draw. D
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16. Document ID: US 6816630 B1

L12: Entry 16 of 29

File: USPT

Nov 9, 2004

US-PAT-NO: 6816630

DOCUMENT-IDENTIFIER: US 6816630 B1

TITLE: System and method for creating and processing data forms

Full	Title	Citation	Front	Review	Classification	Date	Reference	Abstract	Claims	KOMC	Drawn D
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 17. Document ID: US 6754874 B1

L12: Entry 17 of 29

File: USPT

Jun 22, 2004

US-PAT-NO: 6754874

DOCUMENT-IDENTIFIER: US 6754874 B1

TITLE: Computer-aided system and method for evaluating employees

Full	Title	Citation	Front	Review	Classification	Date	Reference	Abstract	Claims	KOMC	Drawn D
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 18. Document ID: US 6748425 B1

L12: Entry 18 of 29

File: USPT

Jun 8, 2004

US-PAT-NO: 6748425

DOCUMENT-IDENTIFIER: US 6748425 B1

TITLE: System and method for browser creation and maintenance of forms

Full	Title	Citation	Front	Review	Classification	Date	Reference	Abstract	Claims	KOMC	Drawn D
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 19. Document ID: US 6728762 B1

L12: Entry 19 of 29

File: USPT

Apr 27, 2004

US-PAT-NO: 6728762

DOCUMENT-IDENTIFIER: US 6728762 B1

TITLE: System and method for browser definition of workflow documents

Full	Title	Citation	Front	Review	Classification	Date	Reference	Abstract	Claims	KOMC	Drawn D
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 20. Document ID: US 6721713 B1

L12: Entry 20 of 29

File: USPT

Apr 13, 2004

US-PAT-NO: 6721713

DOCUMENT-IDENTIFIER: US 6721713 B1

TITLE: Business alliance identification in a web architecture framework

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Drawn D](#)

21. Document ID: US 6684188 B1

L12: Entry 21 of 29

File: USPT

Jan 27, 2004

US-PAT-NO: 6684188

DOCUMENT-IDENTIFIER: US 6684188 B1

TITLE: Method for production of medical records and other technical documents

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Drawn D](#)

22. Document ID: US 6526423 B2

L12: Entry 22 of 29

File: USPT

Feb 25, 2003

US-PAT-NO: 6526423

DOCUMENT-IDENTIFIER: US 6526423 B2

TITLE: System and method for creating, generating and processing user-defined generic specs

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Drawn D](#)

23. Document ID: US 6232970 B1

L12: Entry 23 of 29

File: USPT

May 15, 2001

US-PAT-NO: 6232970

DOCUMENT-IDENTIFIER: US 6232970 B1

TITLE: User interface methodology supporting light data entry for microprocessor device having limited user input

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Drawn D](#)

24. Document ID: US 6226656 B1

L12: Entry 24 of 29

File: USPT

May 1, 2001

US-PAT-NO: 6226656

DOCUMENT-IDENTIFIER: US 6226656 B1

TITLE: System and method for creating, generating and processing user-defined generic specs

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMTC	Drawn D
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25. Document ID: US 6192381 B1

L12: Entry 25 of 29

File: USPT

Feb 20, 2001

US-PAT-NO: 6192381

DOCUMENT-IDENTIFIER: US 6192381 B1

TITLE: Single-document active user interface, method and system for implementing same

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMTC	Drawn D
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26. Document ID: US 6173295 B1

L12: Entry 26 of 29

File: USPT

Jan 9, 2001

US-PAT-NO: 6173295

DOCUMENT-IDENTIFIER: US 6173295 B1

TITLE: Method, system, and program for creating a job ticket including information on components and print attributes of a print job

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMTC	Drawn D
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27. Document ID: US 6157935 A

L12: Entry 27 of 29

File: USPT

Dec 5, 2000

US-PAT-NO: 6157935

DOCUMENT-IDENTIFIER: US 6157935 A

TITLE: Remote data access and management system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMTC	Drawn D
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28. Document ID: US 5890175 A

L12: Entry 28 of 29

File: USPT

Mar 30, 1999

US-PAT-NO: 5890175

DOCUMENT-IDENTIFIER: US 5890175 A

TITLE: Dynamic generation and display of catalogs

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMTC	Drawn D
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29. Document ID: US 5557723 A

L12: Entry 29 of 29

File: USPT

Sep 17, 1996

US-PAT-NO: 5557723

DOCUMENT-IDENTIFIER: US 5557723 A

TITLE: Method and system for customizing forms in an electronic mail system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Abstract	Claims	KMC	Drawn D
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Clear	Generate Collection	Print	Fwd Refs	Bkwd Refs	Generate OACS
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Generate OACS				

Search Results - Record(s) 1 through 12 of 12 returned.

1. Document ID: US 20030110443 A1

Using default format because multiple data bases are involved.

L11: Entry 1 of 12

File: PGPB

Jun 12, 2003

PGPUB-DOCUMENT-NUMBER: 20030110443

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030110443 A1

TITLE: Method and apparatus for programmatic learned routing in an electronic form system

PUBLICATION-DATE: June 12, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Yankovich, Steve	San Jose	CA	US	
Porter, Edwin	Menlo Park	CA	US	
Hoover, Nathan	Los Gatos	CA	US	

US-CL-CURRENT: 715/501.1

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Drawn D](#)

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2. Document ID: US 20030014500 A1

L11: Entry 2 of 12

File: PGPB

Jan 16, 2003

PGPUB-DOCUMENT-NUMBER: 20030014500

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030014500 A1

TITLE: Transactional data communications for process control systems

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Drawn D](#)

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3. Document ID: US 6721747 B2

L11: Entry 3 of 12

File: USPT

Apr 13, 2004

US-PAT-NO: 6721747

DOCUMENT-IDENTIFIER: US 6721747 B2

TITLE: Method and apparatus for an information server

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Draw. D.](#)

4. Document ID: US 6697825 B1

L11: Entry 4 of 12

File: USPT

Feb 24, 2004

US-PAT-NO: 6697825

DOCUMENT-IDENTIFIER: US 6697825 B1

TITLE: Method and apparatus for generating and modifying multiple instances of element of a web site

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Draw. D.](#)

5. Document ID: US 6542925 B2

L11: Entry 5 of 12

File: USPT

Apr 1, 2003

US-PAT-NO: 6542925

DOCUMENT-IDENTIFIER: US 6542925 B2

TITLE: Generation and distribution of motion commands over a distributed network

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Draw. D.](#)

6. Document ID: US 6542912 B2

L11: Entry 6 of 12

File: USPT

Apr 1, 2003

US-PAT-NO: 6542912

DOCUMENT-IDENTIFIER: US 6542912 B2

TITLE: Tool for building documents for commerce in trading partner networks and interface definitions based on the documents

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Draw. D.](#)

7. Document ID: US 6397259 B1

L11: Entry 7 of 12

File: USPT

May 28, 2002

US-PAT-NO: 6397259

DOCUMENT-IDENTIFIER: US 6397259 B1

TITLE: Method, system and apparatus for packet minimized communications

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Draw. D.](#)

8. Document ID: US 6393469 B1

L11: Entry 8 of 12

File: USPT

May 21, 2002

US-PAT-NO: 6393469

DOCUMENT-IDENTIFIER: US 6393469 B1

TITLE: Method and apparatus for publishing hypermedia documents over wide area networks

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. D
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 9. Document ID: US 6338074 B1

L11: Entry 9 of 12

File: USPT

Jan 8, 2002

US-PAT-NO: 6338074

DOCUMENT-IDENTIFIER: US 6338074 B1

TITLE: System for enterprise-wide work flow automation

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. D
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 10. Document ID: US 5870552 A

L11: Entry 10 of 12

File: USPT

Feb 9, 1999

US-PAT-NO: 5870552

DOCUMENT-IDENTIFIER: US 5870552 A

TITLE: Method and apparatus for publishing hypermedia documents over wide area networks

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. D
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 11. Document ID: US 5632022 A

L11: Entry 11 of 12

File: USPT

May 20, 1997

US-PAT-NO: 5632022

DOCUMENT-IDENTIFIER: US 5632022 A

TITLE: Encyclopedia of software components

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. D
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 12. Document ID: US 5339392 A

L11: Entry 12 of 12

File: USPT

Aug 16, 1994

US-PAT-NO: 5339392

DOCUMENT-IDENTIFIER: US 5339392 A

TITLE: Apparatus and method for creation of a user definable video displayed document showing changes in real time data

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Abstract](#) [Claims](#) [KMIC](#) [Drawn D](#)

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